

AMENDMENTS TO THE SPECIFICATION

Please delete the present Abstract of the Disclosure.

Please add the following new Abstract of the Disclosure:

In a multi-supply-voltage semiconductor device including multiple blocks 31, 32, each of which has independent clock circuit 41, 42, and operating with variable power supply-101, variable delay circuit 20-which changes the amount of delay in accordance with the voltage value of the variable power supply 101-is provided to a clock signal supplied to several blocks 32-from clock generator circuit-40. This can reduce clock skew between the blocks even when the power supply voltage of variable power supply 101-is changed.